

COURSE PLAN

Session No	Topics to be covered	Teaching Methodology
UNIT - I		
1	Subject Introduction and Syllabus Discussion	BB
2	Introduction: Views –Goals	BB
3	–Types Of System –	BB
4	OS Structure - Components Of OS	BB
5	Services - System Structures	BB
6	Layered Approach -Virtual Machines	BB
7	System Design And Implementation.	BB
8	Process Management: Process - Process Scheduling	BB
9	Cooperating Process	BB
10	Threads - Inter process Communication	BB
11	CPU Scheduling : CPU Schedulers – Scheduling Criteria	BB
12	Scheduling Algorithms	BB
13	Revision of UNIT I	-
Unit Test - I		
UNIT - II		
14	Process Synchronization:	BB
15	Critical-Section Problem	BB
16	Synchronization Hardware	BB
17	Semaphores –	BB
18	Classic Problems Of Synchronization	BB
19	Critical Region – Monitors	BB
20	Deadlock : Characterization –	BB
21	Methods For Handling Deadlocks-Prevention	BB
22	Avoidance, And Detection Of Deadlock	BB
23	Recovery From Deadlock	BB
24	Revision of UNIT II	-

CIA TEST - I		
UNIT - III		
25	Memory Management : Address Binding	BB
26	Dynamic Loading And Linking	BB
27	Overlays	BB
28	Logical And Physical Address Space	BB
29	Contiguous Allocation -	BB
30	Internal & External Fragmentation.	BB
31	Non Contiguous Allocation: Paging	BB
32	Segmentation Schemes	BB
33	-Implementation - Hardware	BB
34	Protection - Sharing.	BB
35	Fragmentation.	BB
36	Revision of UNIT III	-
Unit Test - III		
UNIT - IV		
37	Virtual Memory : Demand Paging	BB
38,39	Page Replacement - Algorithms	BB
40	Thrashing	BB
41	File System: Concepts -File Access Methods	Discussion
42	Directory Structure	Discussion
43	Protection Consistency Semantics	BB
44	File System Structures	BB
45	Allocation Methods	BB
46	Free Space Management	BB
47	Revision of UNIT IV	-
CIA TEST - II		
UNIT - V		
48	I/O Systems: Overview - I/O Hardware	Presentation

49	Application I/O Interface	Presentation
50	Kernel I/O Subsystem –	Presentation
51	Transforming I/O Requests To Hardware Operation	Presentation
52	Performance.	Presentation
53	Secondary Storage Structures	Presentation
54	Protection – Goals- Domain Access Matrix	BB
55	The Security Problem	BB
56	Authentication – Threats	BB
57	Encryption	BB
58	Revision of UNIT V	-
REVISION		
59	Revision of UNIT I	BB
60	Revision of UNIT II	BB
61	Revision of UNIT III	BB
62	Revision of UNIT IV	BB
63	Revision of UNIT V	BB
CIA TEST - III		

Name & Signature	Prepared by	Approved by
	Mrs.S.Meenakshi Mrs.T.Lavanya Suja	Dr.V.Umadevi
Designation	Asst.Professor / CS	HOD / CS
Date		